

STATCOM Contribution for Improving the Dynamic Performances of HVDC Link with Low SCR under Phase Opening Faults

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Abstract:

In this paper, a new application of STATCOM in HVDC system with low short-circuit ratio SCR has been proposed to solve the disturbance faults due to the interaction between HVDC and AC systems. These interactions generate inevitable drawbacks such as high temporary overvoltage's, low-frequency resonances, risks of voltage instability, long fault recovery times and risk of development of commutation failures. In this paper, the treatment of the phase opening fault at inverter side is studied by integrating of the STATCOM device in HVDC connection. The choice of this application has been justified by several tests at inverter AC side, and this with the aim of improving the performance of the system against commutation failures, re-establishment of functioning and limits the impact of this fault. The performance of the proposed system has been validated by several simulation results under MATLAB / SIMULINK.

1. Introduction

Advances HVDC high voltage direct current transport technology is becoming increasingly required to ensure reliable, healthy and sustainable power supply and to meet the growing needs of the electrical power market, by providing support to the AC connected network.

The advantage of HVDC transmission over long distances has allowed its use on air and submarine domains, as well as in the field of the interconnection of two networks with the same frequency or different frequencies [1], [2].

However, the application of the LCC uses thyristor technology at the line frequency, which requires a large reactive power estimated at 50 % to 60 % of the transported power and also the injection of harmonic currents of low order and the risk of commutation errors at the inverter and their reliance on relatively strong alternative systems to provide commutation voltages [3].

HVDC systems can be classified according to their strengths in the short-circuit ratio (SCR) categories. $SCR > 3$, $2 < SCR < 3$, and $SCR < 2$ correspond respectively to strong, weak and very weak links [4],[5] and [6]. The problems recognized with low and very low HVDC systems are high temporary overvoltage, the risk of voltage instability, low-

frequency resonances, harmonic instability and commutation failures [7] and [8].

The majority of the previous phenomena are closely related to the AC side voltage regulation. Therefore, a special control strategy is adopted by the introduction of FACTS devices (STATCOM and SVC), allowing to provide appropriate solutions to improve the voltage stability of the supply system [9] and [10].

The STATCOM adopts the technology of the voltage source converter (VSC), for regulating a voltage via a transformer connected in shunt to the network. It allows a more robust control than the SVC and it delivers reactive power even in the presence of a very weak voltage. Given its characteristics, the maximum current of the STATCOM is independent of the voltage of the node [11] and [12].

Many faults can occur either at the AC network or at the converter (open circuit) [13]. The AC side faults of the inverter affect the HVDC system, such as those related to short circuit (SC), phase opening (OP) and the voltage drop (VD). Consequently, these defects have always affected the stability of the power conversion systems which minimizes their reliability.

Several researchers have dealt with the subject of the integration of the HVDC system inverter

STATCOM by various techniques to treat the faults mentioned previously [3], [14], [15], [16], [17] and [18]. They have led to an effective control of the magnitude of the fault current as well as the control of commutation failures problems. However, a minority of researches concerning phase-opening faults related to converter- and AC-side voltage imbalance have not been sufficiently treated.

2.1 Description of CIGRE HVDC benchmark model

The CIGRE HVDC system is a single pole HVDC link (500 kV - 1000 MW) of two converters 12 pulses, one works as a rectifier and the other as an inverter.

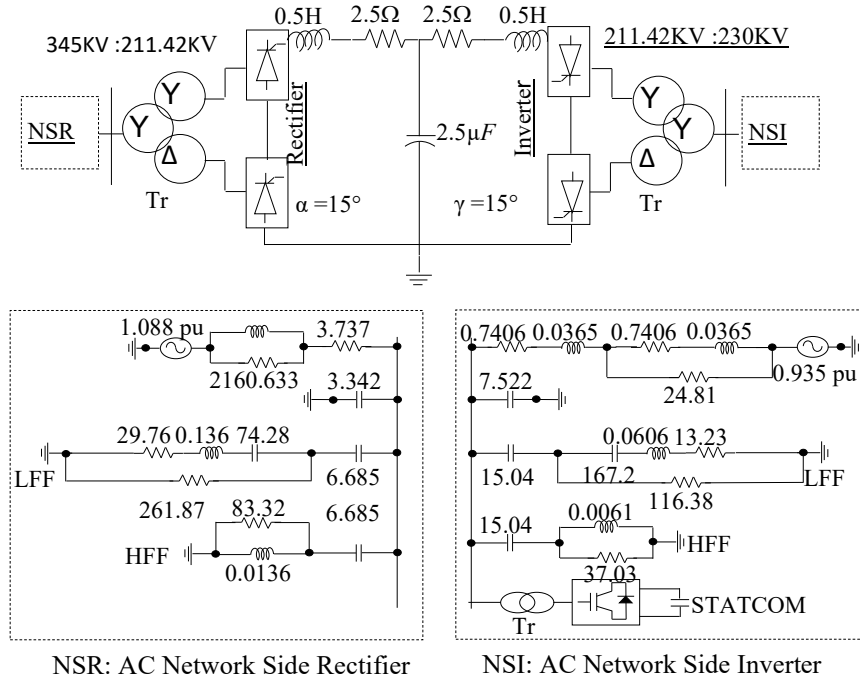


Figure 1. HVDC-STATCOM system.

In this article, a new application of phase opening fault at the inverter side is proposed. This contribution can respond favourably to the problem mentioned above and improve the stability of the HVDC system. Firstly, STATCOM device integration at the AC side of the CIGRE HVDC model inverter is performed, next applying several faults to test the performance of this integration. Finally, the faults effect in HVDC with and without STATCOM system is discussed.

2. Methodologies

The proposed system (HVDC-STATCOM) is presented in Fig.1 It consists of an HVDC LCC link based on the first CIGRE HVDC benchmark model and a connected STATCOM device, which is connected to the inverter node via a transformer [14], [16] and [19]. The purpose of the STATCOM integration is to set the switching voltage required to the HVDC inverter, by the compensation of the reactive power to the AC grid, in steady state, and under dynamic conditions.

These converters are connected to the AC systems, and each system has a short-circuit ratio SCR of 2.5 at a frequency of 50 Hz, which represents the degree of system strength. The model also includes AC filters and capacitor banks for reactive power compensation. The DC transmission line is modeled in "T", with high shunt capacitance and low series inductance. More details of the CIGRE HVDC model have been addressed in the references [19]. Table 1 presents CIGRE HVDC benchmark system parameters.

Table 1. Different characteristics CIGRE HVDC.

parameters	Rectifier	Inverter
AC Voltage Base	345 KV	230 KV
Base MVA	100M VA	100 MVA
Transf. Tap (HV side)	1.01 pu	0.989 pu
Nominal DC Voltage	500 KV	500 KV
Nominal DC Current	2 KA	2 KA
Nominal DC Power	1000 MW	1000 MW
Voltage Source	1.088 pu	0.935 pu
Transf. Xl	0.18 pu	0.18 pu
Frequency	50 Hz	50 Hz
Angle Min	$\alpha=15^\circ$	$\gamma=15^\circ$

2.2 STATCOM Operation

In this study, the synchronous static compensator (STATCOM) is connected to the inverter node of the HVDC link. It consists of a two-stage, three-phase inverter and a 3000 μF capacitor which behaves as a variable DC voltage source.

Fig.2 shows an equivalent single-phase circuit of STATCOM where V_L is the phase source voltage. The models of the transmission line are the shunt resistor r_{sh} and the leakage inductance L_{sh} of the transformer. V_{sh} is the AC output voltage of the STATCOM.

In addition, the transmission system is assumed to be symmetrical, the saturation of the transformer, the delays of the regulator and the non-linearities caused by the switching of the semiconductor devices are also assumed to be negligible in the equivalent circuit [10], [20] and [21].

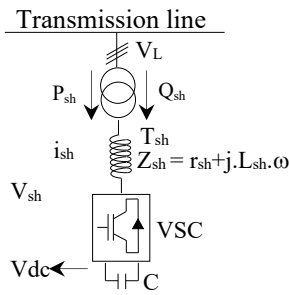


Figure 2. Configuration of STATCOM.

The equations of system in Park's transformation model are presented as follows:

$$\frac{d}{dt} \begin{bmatrix} i_{shd} \\ i_{shq} \end{bmatrix} = \begin{bmatrix} \frac{r_{sh}}{L_{sh}} & \omega \\ -\omega & \frac{r_{sh}}{L_{sh}} \end{bmatrix} \begin{bmatrix} i_{shd} \\ i_{shq} \end{bmatrix} + \frac{1}{L_{sh}} \left(\begin{bmatrix} V_{shd} \\ V_{shq} \end{bmatrix} - \begin{bmatrix} V_{rd} \\ V_{rq} \end{bmatrix} \right) \quad (1)$$

The active and reactive powers are expressed by the following equations:

$$\begin{cases} P_{sh} = \frac{3}{2} (V_{shd} i_{shd} + V_{shq} i_{shq}) & (2) \\ Q_{sh} = \frac{3}{2} (V_{shd} i_{shq} - V_{shq} i_{shd}) & (3) \end{cases}$$

3. Strategies of HVDC control

3.1 Rectifier current control

Fig.3 shows the control of the rectifier current by a PI regulator. The output of this regulator is known as the delay angle or firing angle that generates the firing pulses of the rectifier. This angle is limited in the interval $[\alpha_{min}, \alpha_{max}]$, $\alpha_{min} = 5^\circ$ and $\alpha_{max} = 165^\circ$ [1].

3.2 Inverter control system

The control system of the inverter is shown in Fig.4. It consists of two regulators: one for voltage control and the other for transient current control [1-4]. The direct current I_{di} is compared with the current I_r and the result, in turn, is compared with the margin of the current I_m (0.1). The regulator PI generates the control angle α_1 which is limited between $\alpha_{min} = 92^\circ$ and $\alpha_{max} = 165^\circ$ [1].

The voltage controller compares the measured voltage with the reference value V_{ref} (1 pu). The error is transmitted to a PI regulator which generates the control angle α_2 , limited between α_{min} and α_{max} [1-5]. The smaller of the values α_1 and α_2 are used to generate the ignition pulses. In normal operation, α_1 is greater than α_2 and in transient, α_1 is less than α_2 .

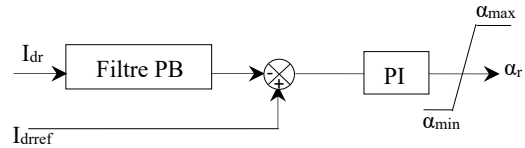


Figure 3. Rectifier current control.

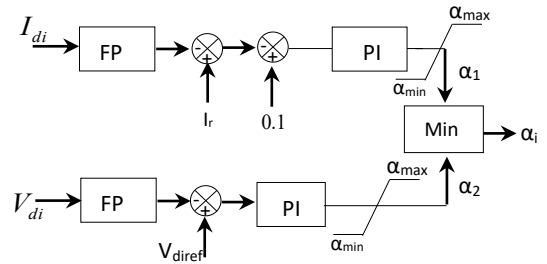


Figure 4. Inverter control system.

4. STATCOM control strategy

In this strategy, the voltage of the shunt converter STATCOM is decomposed into two components. One component is in phase and the other in quadrature with the voltage at the connection point of the STATCOM (V_L). The decoupled control system is used at the same time to control the voltage V_L and the DC voltage V_{dc} of the capacitor [21] and [22].

4.1 Current regulation

The Figure 5 describes the circuit for adjusting the current in which the coupling terms are injected with opposite signs in order to make the two currents (I_{dsh} and I_{qsh}) completely independent and that in order to separate between the active and reactive powers injected.

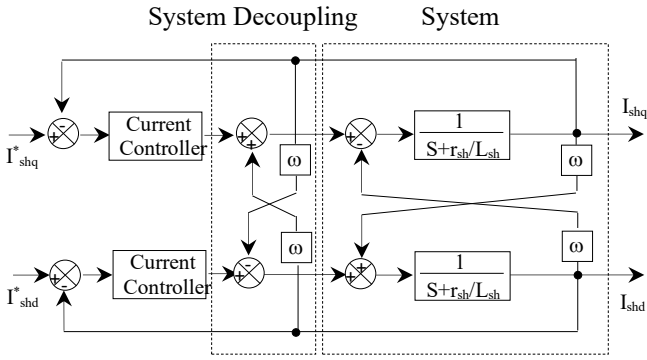


Figure 5. System Decoupling

Equation (1) shows the interaction between the current loops caused by the coupling term ωL [10], [22] and [23]. The principle of this control strategy is to convert three-phase measured currents and voltages into dq values to compute the current references from equations (4) and (5) based on equations (2) and (3):

$$i_{shd}^* = \frac{2}{3} \frac{(P_{sh}^* V_{shd} - Q_{sh}^* V_{shq})}{V_{shd}^2 + V_{shq}^2} \quad (4)$$

$$i_{shq}^* = \frac{2}{3} \frac{(P_{sh}^* V_{shq} - Q_{sh}^* V_{shd})}{V_{shd}^2 + V_{shq}^2} \quad (5)$$

4.2 DC voltage regulation

The DC voltage of the STATCOM is expressed by:

$$V_{dc}^2 = \frac{P_{sh}}{CS} \quad (8)$$

Fig.6 describes the control of the DC voltage

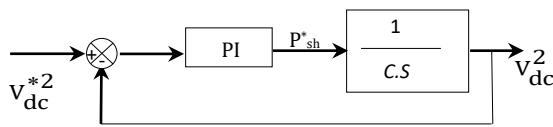


Figure 6. Dc voltage control.

Figure 7. shows the overall control of STATCOM

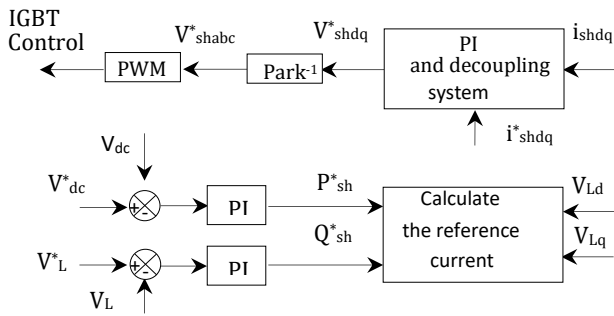


Figure 7. Overall control of STATCOM.

5. A comparative study between HVDC and HVDC-STATCOM under phase opening faults.

In this part, a fault is applied in the AC line at the inverter side (phase opening fault). There are two faults: single-phase fault and three-phase fault. This fault is experienced on the one hand with the HVDC and on the other with the HVDC-STATCOM in order to know their behavior. A comparison between the performances of the two systems is made to judge their efficiency.

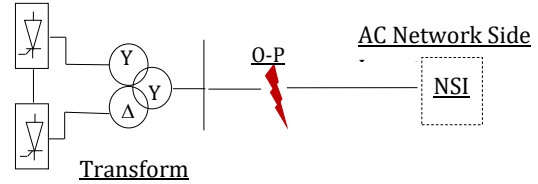


Figure 8. HVDC and HVDC-STATCOM under phase opening faults.

5.1 HVDC with and without STATCOM under phase open fault (single phase)

From Fig.9a and Fig.9b, it is noted that during the steady state, the voltages V_{rms} and the currents I_{rms} retain their behavior and remain stable. After applying the fault at the moment 1s for the duration of 100 ms, the V_{rms} reaches a peak of 2pu and takes recovery time of 0.6s in the case of HVDC alone. On the other hand, the voltage approaches peak value of 1.9 pu and a recovery time of 0.3s in the case of HVDC-STATCOM. For the current I_{rms} , we note that the recovery time for both systems (HVDC, HVDC-STATCOM) has the same behavior as the case of Figure 9a.

It is also observed that the HVDC system generates undulations whose period is less than 0.08 s compared with HVDC-STATCOM system.

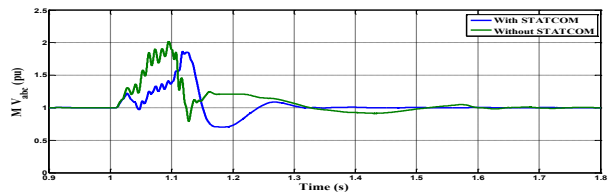


Figure 9a. Voltage with and without STATCOM due to single-phase opening fault at inverter AC side.

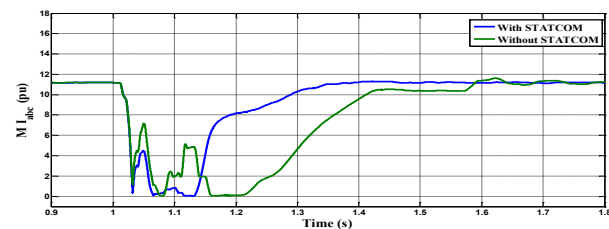


Figure 9b. Current with and without STATCOM due to single-phase opening fault at inverter AC side.

In Fig.10a and Fig.10b, note that the voltage supplied by the affected AC grid, drops the DC voltage at the input of the inverter and the current I_d has peaks of 2.6 pu for both systems (HVDC, HVDC-STATCOM). In this case, the rectifier goes into inverter mode (the angle α exceeds 90°) and the reference current I_d decreases to 0.3 pu, following the intervention of the VDCOL after detection of a DC voltage drop below 0, 6 pu (threshold voltage). The recovery times (T_r) of the DC voltage and current (V_d , I_d) in both systems (HVDC, HVDC-STATCOM) are respectively (0.53s, 0.25s) and (0.58s, 0.3s). It is also noted that the current I_d in the case of the HVDC system alone, has high peaks around the value 1.8 contrary to the peaks presented by the HVDC-STATCOM system which have smaller values and tend towards zero.

In Figure 10c, it is clearly seen that the recovery time of the continuous power transported for the HVDC-STATCOM system is faster than that of the HVDC system alone with a difference of 0.3s.

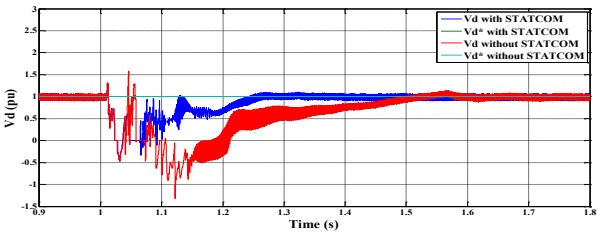


Figure 10a. HVDC link voltages with and without STATCOM due to single-phase opening fault at inverter AC side.

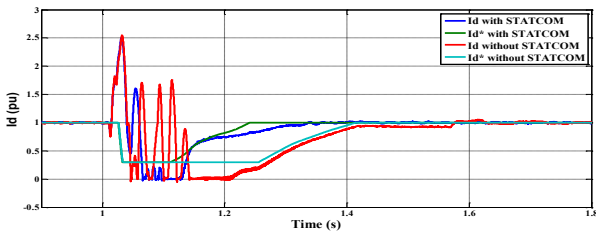


Figure 10b. HVDC link currents with and without STATCOM due to single-phase opening fault at inverter AC side.

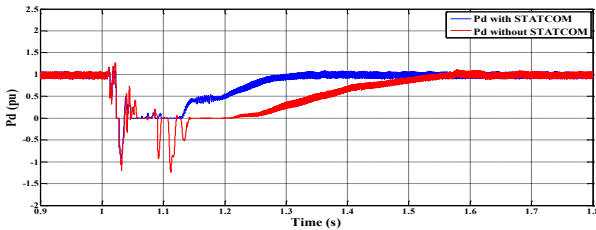
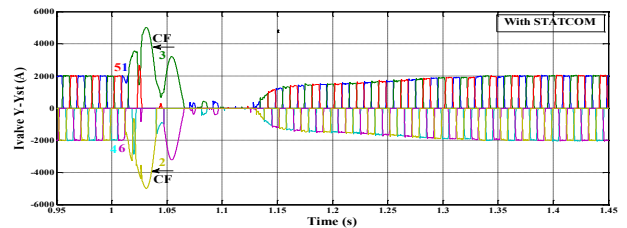
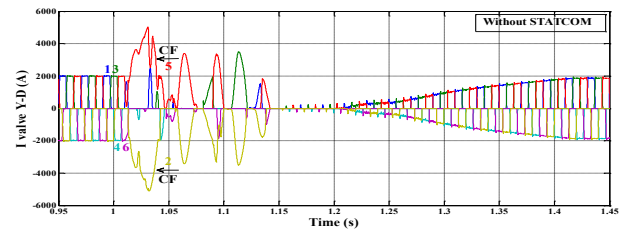
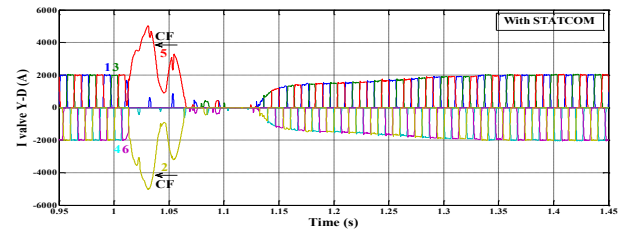
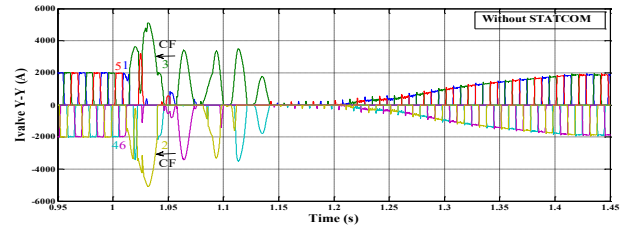


Figure 10c. HVDC link power with and without STATCOM due to single-phase opening fault at inverter AC side.

Fig.11a indicates that the functioning of valves 1 to 6 is ordered and stable before the fault. After the fault appearance, a malfunctioning is observed at the valves 3 and 2 because of the direct current I_d increase (Fig 9b), which generated significant peaks

of the current of these valves. This fault, in turn, created a disorder of functioning at the other valves for a duration of 0.18 s. Beyond $t = 1.2$ s, the functioning restoration of the valves is noticed and the steady state is happening from $t = 1.4$ s.

In Fig. 11b, the same observation of Fig.10a is drawn for the behavior of valves 3 and 2 with a time interval of malfunctioning of $t = 0.11$ s and the correct functioning re-establishment (orders of valves) from 1.13s and maintaining of the steady state from $t = 1.15$ s. Fig.11c shows that the valves 5 and 2 work in concordance, so they conduct the current at the same time, which represents a failure commutation compared to the standards of the real regime. This failure is interpreted as a significant increase in DC current I_d followed by disturbances during 0.18 s and re-establishment of the correct functioning (valve orders) from 1.2 s and maintaining of the steady state from 1.4s. In Fig.11.d, the same observations are drawn as those in Fig.10.c with disturbances whose duration is less than that of the case HVDC (0.05s). The re-establishment of the correct functioning (orders of valves) is faster (1.13 s) and maintaining of the steady state from $t = 1.15$ s.



Figures 11a, b, c, d. Currents in the valves of the two bridges Graetz (YY and $Y\Delta$) with and without STATCOM.

5.1 HVDC with and without STATCOM under phase open fault (three phases)

In Fig.12.a, the three-phase fault is applied at the moment 1s for the duration of 100 ms. The insertion of STATCOM has a perfect reduction effectiveness. The voltage V_{rms} peak and the recovery time are respectively reduced (from 3.8 pu to 1.5 pu) and (from 1s to 0.22 s).

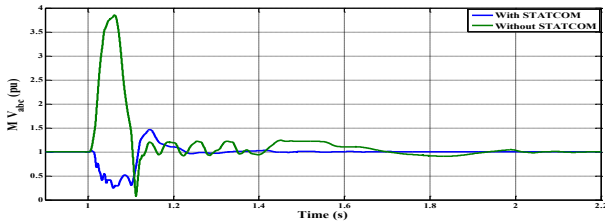


Figure 12a. Voltage with and without STATCOM following an opening fault of 3 phases on the AC side of the inverter.

Fig.12.b shows clearly that the presence of the STATCOM has reduced the undulations and recovery time of the current I_{rms} (from 1s to 0.22 s), compared to the HVDC system alone.

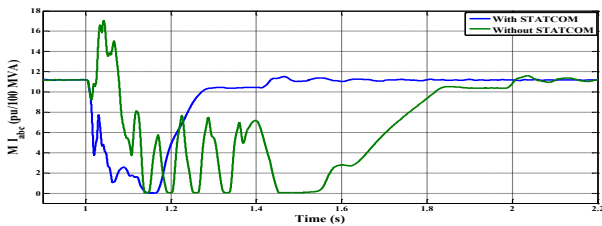


Figure 12b. Current with and without STATCOM following a fault opening of 3 phases on the AC side of the inverter.

In Fig.13a, Fig.13b and Fig.13c, it is observed that the use of STATCOM improves, on the one hand, the recovery time of the functioning after the elimination of the fault (the time recovery time goes from 1s to 0.4s) and on the other hand, it reduces the undulations appearing on the V_d , I_d and P_d magnitudes of the HVDC system alone.

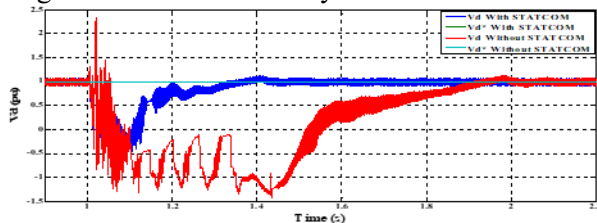


Figure 13a. Voltage of the HVDC link with and without STATCOM due to a fault in the 3-phase AC side opening of the inverter.

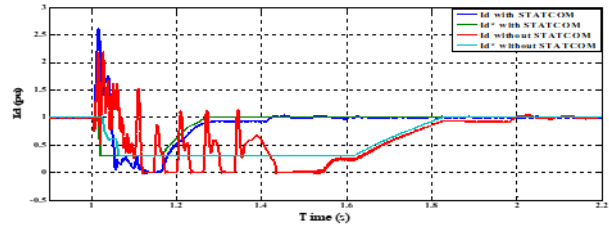


Figure 13b. Current of the HVDC link with and without STATCOM due to a fault in the 3-phase AC side opening of the inverter.

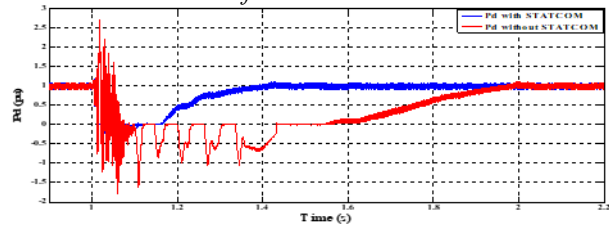
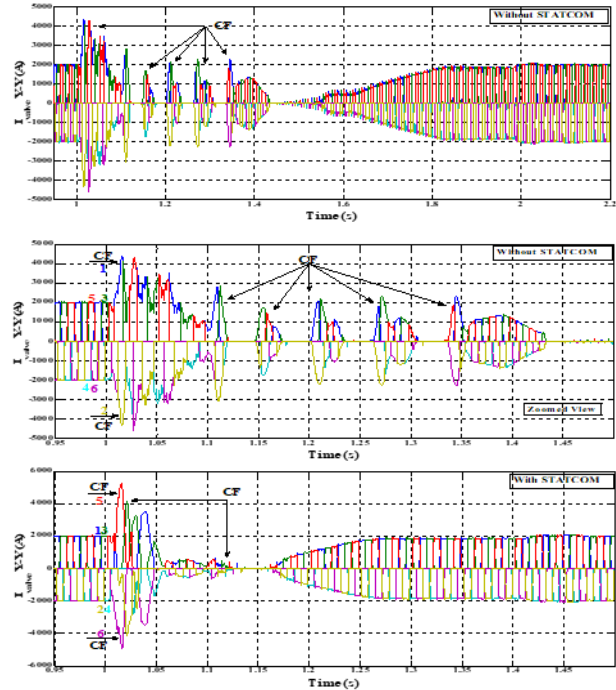


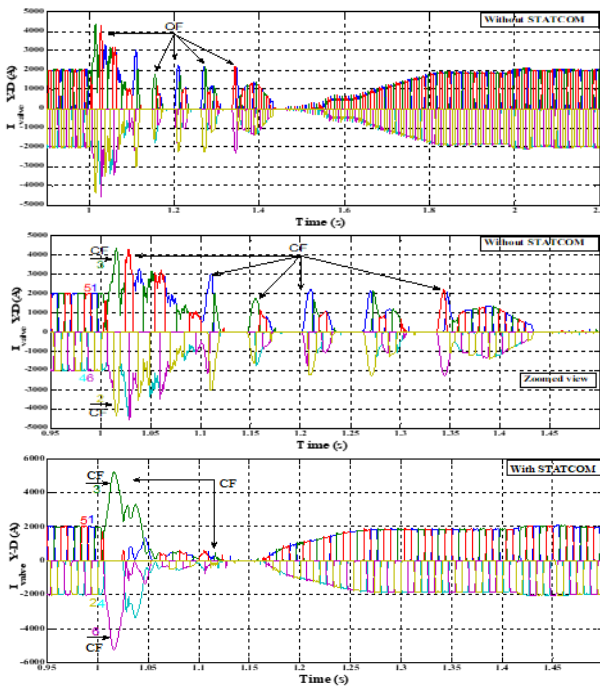
Figure 13c. HVDC link power with and without STATCOM due to a fault in the 3-phase AC side opening of the inverter.

In Figs.14.a, b,c, and Fig.15.a, b,c, the normal and stable functioning of all the valves is observed before the fault occurrence.



Figures 14.a, b, c. Currents in the valves of bridge Graetz (YY) with and without STATCOM due to 3 phase opening faults at inverter AC side

After that, the increase of the DC current I_d has created significant peaks of the current and consequently a disorder of valves functioning. The functioning recovery of these valves is better in HVDC-STATCOM case whose steady state is re-established from the moment $t = 1.25$ s compared to that of HVDC alone which is from 1.8s.



Figures 15a, b, c. Currents in the valves of bridge Graetz (Δ) with and without STATCOM due to 3 phases opening fault at inverter AC side

6. Conclusion

In this paper, a new technique based on the application of STATCOM in HVDC system with low SCR is proposed. This technique solved the disturbances caused by the interaction between the HVDC and AC systems during the phase opening faults by adding the STATCOM to the HVDC system at the inverter AC side. The solutions carried by the new HVDC-STATCOM system have improved the dynamic performance during various faults.

The results of the simulation demonstrated the robustness and efficiency of this technique by obtaining satisfactory responses: faster recovery time after the fault as well as a better minimization of currents and voltages disturbances in the case of HVDC-STATCOM system.

Author Statements:

- **Ethical approval:** The conducted research is not related to either human or animal use.
- **Conflict of interest:** The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper
- **Acknowledgement:** The authors declare that they have nobody or no-company to acknowledge.

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- **Use of AI Tools:** The author(s) declare that no generative AI or AI-assisted technologies were used in the writing process of this manuscript.

References

- [1] S.A Zidi, S Hadjeri, M.K Fellah, "Dynamic Performance of an HVDC Link", Journal of Electrical Systems, issue 1-3, pp. 15-23, 2005
- [2] Roberto Rudervall, J.P. Charpentier, Raghuveer Sharma, "High Voltage Direct Current (HVDC) Transmission Systems Technology Review Paper", Energy Week 2000, Washington, D.C, USA, March 7- 8, 2000.
- [3] Chan-Ki, K, "Dynamic coordination strategies between HVDC and STATCOM", IEEE Transmission & Distribution Conference & Exposition: Asia and Pacific - Seoul, South Korea, pp. 1- 9, Dec 2009.
- [4] R.S. Thallam, "Review of the design and performance features of HVDC systems connected to low short circuit ratio AC systems", IEEE Transactions on Power Delivery, Vol.7, No.4, pp. 2065 - 2073, Oct 1992.
- [5] J. Arrillaga, "High Voltage Direct Current Transmission", ISBN 0-852969-41-4, The Institution of Electrical Engineers, 1998.
- [6] Mohamed Khatir, Sid.Ahmed Zidi, Samir Hadjeri, Mohammed.Karim Fellah, Omar Dahou, " Effect of the DC Control on Recovery from Commutation Failures in an HVDC Inverter Feeding a Weak AC Network", Journal of Electrical Engineering, Vol. 58, No. 4, pp. 200-206, 2007.
- [7] O.B. Nayak, A.M. Gole, D.G. Chapman and J.B. Davies, "Dynamic Performance of Static and Synchronous Compensators at an HVDC Inverter bus in a Very Weak ac System", IEEE Transactions on Power Systems, Vol.9, No.3, PP.1350 – 1358, Aug 1994.
- [8] C.Guo, Yi Zhang, Aniruddha M. Gole, and Chengyong Zhao, "Analysis of Dual-Infeed HVDC With LCC–HVDC, and VSC–HVDC", IEEE Transactions on power delivery, vol. 27, no. 3, pp. 1529– 1537, July 2012.
- [9] J. Dixon, L. Moran, J. Rodriguez and R. Domke, "Reactive Power Compensation Technologies: State-of-the-Art Review", Proceedings of the IEEE, Vol.93, No.12, pp. 2144 – 2164, Dec 2005.
- [10] M.S. El-Moursi, A.M. Sharaf.: Novel reactive power controllers for the STATCOM and SSSC. In: Electric Power Systems Research, 76: pp. 228–241, 2006.

- [11] B. Gultekin and M. Ermis, "Cascaded Multilevel Converter-Based Transmission STATCOM: System Design Methodology and Development of a 12 kV \pm 12 MVAR Power Stage," *IEEE Transactions on Power Electronics*, vol. 28, no. 11, pp. 4930-4950, 2013.
- [12] R. Xu, Y. Yu, R. Yang, G. Wang, D. Xu, B. Li, and S. Sui, "A Novel Control Method for Transformerless H-Bridge Cascaded STATCOM With Star Configuration," *IEEE Transactions on Power Electronics*, vol. 30, no. 3, pp. 1189-1202, 2015.
- [13] D.-E. Kim and D.-C. Lee, "Fault diagnosis of three-phase PWM inverters using wavelet and SVM," *Journal of Power Electronics*, Vol. 9, No. 3, pp. 377-385, May 2009.
- [14] Wei Li and Xiangning Xiao, "Impact of STATCOM on the Voltage Stability of HVDC Terminating at Location Having Low SCR", *IEEE International Conference on Power System Technology (POWERCON) - Wollongong, Australia*, pp. 1 - 6, Nov 2016.
- [15] Mohamed Khatir, Sid-Ahmed Zidi, Mohammed-Karim Fellah, Samir Hadjeri and Mohamed Flitti, "The impact study of a STATCOM on commutation failures in an HVDC inverter feeding a weak AC system" *Journal of Electrical Engineering*, VOL. 63, NO. 2, pp. 95-102, 2012.
- [16] Chan-Ki Kim, Jin-Young Kim, Sung-Doo Lee and Eung-Bo Sim, "Stability Enhancement in HVDC System with STATCOM", *Journal Engineering*, Vol. 3, pp. 1072-1081, Nov 2011.
- [17] Qingqing Zheng, Xuan Wang, Yongsheng Fu, Hui Yan, Zhujian Ou, Guangzhou Wang, Yubin Wang, "A STATCOM Compensation Scheme for Suppressing Commutation Failure in HVDC", *Industrial Electronics Society, IECON 2016 - 42nd Annual Conference of the IEEE*, Conference Location: Florence, Italy, pp. 1081-1086, Dec 2016.
- [18] H. Hamlaoui, B. Francois, "Interest of storage based STATCOM systems to the power quality enhancement of thyristors based LCC HVDC links for an offshore wind farm", *IEEE International Conference on Industrial Technology (ICIT) - Conference Location: Lyon, France*, pp. 1702- 1707, Feb 2018.
- [19] M. O. Faruque, Yuyan Zhang, and Venkata Dinavahi, "Detailed Modeling of CIGRÉ HVDC Benchmark System Using PSCAD/EMTDC and PSB/SIMULINK", *IEEE Transactions on Power Delivery*, Vol. 21, No. 1, pp. 378-387, Jan 2006.
- [20] Pereira M., Retzmann D., Lottes .I., Wiesinger M., Wong, G.; "SVC PLUS: An MMC STATCOM for network and grid access applications[J]". in *Power Tech, 2011 IEEE Trondheim*, vol., no., pp. 1- 5, 19-23 June 2011.
- [21] Mohammadi H.P, Bina M, "A Transformerless Medium-Voltage STATCOM Topology Based on Extended Modular Multilevel Converters", in *Power Electronics, IEEE Transactions on*, vol.26, no.5, pp.1534-1545, May 2011.
- [22] S. Krishna, "Stability analysis of Static Synchronous Compensator with a reactive current controller". In: *Electric Power Systems Research*, 78: pp. 1053–1068, 2008.
- [23] Ghadir Radman, Reshma S. Raje, "Dynamic model for power systems with multiple FACTS controllers". In: *Electric Power Systems Research* 78: pp. 361–371, 2008.